



# PocketBeagle P2 Header PinMux Modes

P1 Pin	Label	mode0	mode1	mode2	mode3	mode4	mode5	mode6	mode7	P1 Pin2
P2.01	PWM1A	gpmc_a2	gmii2_txd3	rgmii2_td3	mmc2_dat1	gpmc_a18	pr1_mii1_txd2	ehrpwm1A	gpio1_18	P2.01
P2.02	59	gpmc_a11	gmii2_rxd0	rgmii2_rd0	rmii2_rxd0	gpmc_a27	pr1_mii1_rxer	mcasp0_axr1	gpio1_27	P2.02
P2.03	23	gpmc_d9	lcd_data22	mmc1_dat1	mmc2_dat5	ehrpwm2B	pr1_mii0_col	-	gpio0_23	P2.03
P2.04	58	gpmc_a10	gmii2_rxd1	rgmii2_rd1	rmii2_rxd1	gpmc_a26	pr1_mii1_rxdv	mcasp0_axr0	gpio1_26	P2.04
P2.05	U1_RX	gpmc_wait0	gmii2_crs	gpmc_csn4	rmii2_crs_dv	mmc1_sdcd	pr1_mii1_col	uart4_rxd	gpio0_30	P2.05
P2.06	57	gpmc_a9	gmii2_rxd2	rgmii2_rd2	mmc2_dat7 / rmii2_crs_dv	gpmc_a25	pr1_mii_mr1_clk	mcasp0_fsx	gpio1_25	P2.06
P2.07	U1_TX	gpm_wp	gmii2_rxerr	gpmc_csn5	rmii2_rxerr	mmc2_sdcd	pr1_mii1_txen	uart4_txd	gpio0_31	P2.07
P2.08	60	gpmc_be1n	gmii2_col	gpmc_csn6	mmc2_dat3	gpmc_dir	pr1_mii1_rxlink	mcasp0_aclkr	gpio1_28	P2.08
P2.09	I2C1_SCL	uart1_txd	mmc2_sdwp	dcan1_rx	I2C1_SCL	-	pr1_uart0_txd	pr1_pru0_pru_r31_16	gpio0_15	P2.09
P2.10	52	gpmc_a4	gmii2_txd1	rgmii2_td1	rmii2_txd1	gpmc_a20	pr1_mii1_txd0	eQEP1A_in	gpio1_20	P2.10
P2.11	I2C1_SDA	uart1_rxd	mmc1_sdwp	dcan1_tx	I2C1_SDA	-	pr1_uart0_rxd	pr1_pru1_pru_r31_16	gpio0_14	P2.11
P2.12	PB	POWER								P2.12
P2.13	VOUT	VOUT-5V								P2.13
P2.14	BAT +	VIN-BAT								P2.14
P2.15	GND	GND								P2.15
P2.16	BAT -	BAT-TEMP								P2.16
P2.17	65	gpmc_clk	lcd_memory_clk	gpmc_wait1	mmc2_clk	pr1_mii1_crs	pr1_mdio_mdclk	mcasp0_fsr	gpio2_01	P2.17
P2.18	47	gpmc_ad15	lcd_data16	mmc1_dat7	mmc2_dat3	eQEP2_strobe	pr1_ecap0_ecap_capin_apwm_o	pr1_pru0_pru_r31_15	gpio1_15P	P2.18
P2.19	27	gpmc_ad11	lcd_data20	mmc1_dat3	mmc2_dat7	ehrpwm0_synco	pr1_mii0_txd3	-	gpio0_27	P2.19
P2.20	64	gpmc_csn3	gpmc_a3	rmii2_crs_dv	mmc2_cmd	pr1_mii0_crs	pr1_mdio_data	EMU4	gpio2_00	P2.20
P2.21	GND	GND								P2.21
P2.22	46	gpmc_ad14	lcd_data17	mmc1_dat6	mmc2_dat2	eQEP2_index	pr1_mii0_txd0	pr1_pru0_pru_r31_14	gpio1_14	P2.22
P2.23	+3.3V	VOUT-3.3V								P2.23
P2.24	48	gpmc_ad12	lcd_data19	mmc1_dat4	mmc2_dat0	eQEP2A_in	pr1_mii0_txd2	pr1_pru0_pru_r30_14	gpio1_12	P2.24
P2.25	SPI1_MOSI	uart0_rtsn	uart4_txd	dcan1_rx	I2C1_SCL	spi1_d1	spi1_cs0	pr1_edc_sync1_out	gpio1_09	P2.25
P2.26	RST	nRESETIN_OUT	-	-	-	-	-	-	-	P2.26
P2.27	SPI1_MISO	uart0_ctsn	uart4_rxd	dcan1_tx	I2C1_SDA	spi1_d0	timer7	pr1_edc_sync0_out	gpio1_08	P2.27
P2.28	PRU0_6	mcasp0_axr1	eQEP0_index	-	mcasp1_axr0	EMU3	pr1_pru0_pru_r30_6	pr1_pru0_pru_r31_6	gpio3_20	P2.28
P2.29	SPI1_CLK	eCAP0_in_PWM0_out	uart3_txd	spi1_cs1	pr1_ecap0_ecap_capin_apwm_o	spi1_sclk	mmc0_sdwp	xdma_event_intr2	gpio0_7	P2.29
P2.30	PRU0_3	mcasp0_ahclkr	ehrpwm0_syncl	mcasp0_axr2	spi1_cs0	eCAP2_in_PWM2_out	pr1_pru0_pru_r30_3	pr1_pru0_pru_r31_3	gpio3_17	P2.30
P2.31	SPI1_CS	xdma_event_intr0	-	timer4	clkout1	spi1_cs1	pr1_pru1_pru_r31_16	EMU2	gpio0_19	P2.31
P2.32	PRU0_2	mcasp0_axr0	ehrpwm0_tripzone_input	-	spi1_d1	mmc2_sdcd	pr1_pru0_pru_r30_2	pr1_pru0_pru_r31_2	gpio3_16	P2.32
P2.33	45	gpmc_ad13	lcd_data18	mmc1_dat5	mmc2_dat1	eQEP2B_in	pr1_mii0_txd1	pr1_pru0_pru_r30_15	gpio1_13	P2.33
P2.34	PRU0_5	mcasp0_fsr	eQEP0B_in	mcasp0_axr3	mcasp1_fsx	EMU2	pr1_pru0_pru_r30_5	pr1_pru0_pru_r31_5	gpio3_19	P2.34
P2.35	A5/86	ain5								P2.35
P2.35	A5/86	lcd_vsync	gpmc_a8	gpmc_a1	pr1_edio_data_in2	pr1_edio_data_out2	pr1_pru1_pru_r30_8	pr1_pru1_pru_r31_8	gpio2	P2.35
P2.36	Unknown	Missing in docs.								P2.36
P1 Pin	Label	mode0	mode1	mode2	mode3	mode4	mode5	mode6	mode7	P1 Pin