



# PocketBeagle PRU Output P1/P2 Header PinMux Modes

PRU 0 Outputs	P1/P2 Pin	Offset	mode0	mode1	mode2	mode3	mode4	mode5	mode6	mode7
PRU0_R30_7	P1.29	0x9AC	mcasp0_ahclkx	eQEPO_strobe	mcasp0_axr3	mcasp1_axr1	EMU4	pr1_pru0_pru_r30_7	pr1_pru0_pru_r31_7	gpio3_21
PRU0_R30_4	P1.31	0x9A0	mcasp0_aclkr	eQEPOA_in	mcasp0_axr2	mcasp1_aclkr	mmc0_sdwp	pr1_pru0_pru_r30_4	pr1_pru0_pru_r31_4	gpio3_18
PRU0_R30_1	P1.33	0x994	mcasp0_fsx	ehrpwm0B	-	spi1_d0	mmc1_sdccl	pr1_pru0_pru_r30_1	pr1_pru0_pru_r31_1	gpio3_15
PRU0_R30_0	P1.36	0x990	mcasp0_aclkr	ehrpwm0A	-	spi1_sclk	mmc0_sdccl	pr1_pru0_pru_r30_0	pr1_pru0_pru_r31_0	gpio3_14
PRU0_R30_14	P2.24	0x830	gpmc_ad12	lcd_data19	mmc1_dat4	mmc2_dat0	eQEP2A_in	pr1_mii0_txd2	pr1_pru0_pru_r30_14	gpio1_12
PRU0_R30_6	P2.28	0x9A8	mcasp0_axr1	eQEPO_index	-	mcasp1_axr0	EMU3	pr1_pru0_pru_r30_6	pr1_pru0_pru_r31_6	gpio3_20
PRU0_R30_3	P2.30	0x99C	mcasp0_ahclkx	ehrpwm0_synci	mcasp0_axr2	spi1_cs0	eCAP2_in_PWM2_out	pr1_pru0_pru_r30_3	pr1_pru0_pru_r31_3	gpio3_17
PRU0_R30_2	P2.32	0x998	mcasp0_axr0	ehrpwm0_tripzone_input	-	spi1_d1	mmc2_sdccl	pr1_pru0_pru_r30_2	pr1_pru0_pru_r31_2	gpio3_16
PRU0_R30_15	P2.33	0x834	gpmc_ad13	lcd_data18	mmc1_dat5	mmc2_dat1	eQEP2B_in	pr1_mii0_txd1	pr1_pru0_pru_r30_15	gpio1_13
PRU0_R30_5	P2.34	0x9A4	mcasp0_fsr	eQEPOB_in	mcasp0_axr3	mcasp1_fsx	EMU2	pr1_pru0_pru_r30_5	pr1_pru0_pru_r31_5	gpio3_19

PRU 1 Outputs	P1/P2 Pin	Offset	mode0	mode1	mode2	mode3	mode4	mode5	mode6	mode7
PRU1_R30_9	P1.02	0x8E4	lcd_hsync	gpmc_a9	gpmc_a2	pr1_edio_data_in3	pr1_edio_data_out3	pr1_pru1_pru_r30_9	pr1_pru1_pru_r31_9	gpio2_23
PRU1_R30_11	P1.04	0x8EC	lcd_ac_bias_en	gpmc_a11	pr1_mii1_crs	pr1_edio_data_in5	pr1_edio_data_out5	pr1_pru1_pru_r30_11	pr1_pru1_pru_r31_11	gpio2_25
PRU1_R30_15	P1.30	0x88c	uart0_txd	spi1_cs1	dcan0_rx	I2C2_SCL	eCAP1_in_PWM1_out	pr1_pru1_pru_r30_15	pr1_pru1_pru_r31_15	gpio1_11
PRU1_R30_14	P1.32	0x888	uart0_rxd	spi1_cs0	dcan0_tx	I2C2_SDA	eCAP2_in_PWM2_out	pr1_pru1_pru_r30_14	pr1_pru1_pru_r31_14	gpio1_10
PRU1_R30_10	P1.35	0x8E8	lcd_pclk	gpmc_a10	pru_mii0_crs	pr1_edio_data_in4	pr1_edio_data_out4	pr1_pru1_pru_r30_10	pr1_pru1_pru_r31_10	gpio2_24
PRU1_R30_8	P2.35	0x8E0	lcd_vsync	gpmc_a8	gpmc_a1	pr1_edio_data_in2	pr1_edio_data_out2	pr1_pru1_pru_r30_8	pr1_pru1_pru_r31_8	gpio2_22

The information above shows the Pins on the PocketBeagle P1 and P2 Headers which can be used in PRU0 or PRU1 as Outputs.